

WEST Search History

DATE: Saturday, August 14, 2004

Hide? Set Name Query**Hit Count***DB=USPT; PLUR=YES; OP=ADJ*

| | | | |
|--------------------------|-----|------------------------------------------------------------|------|
| <input type="checkbox"/> | L16 | L12 near7 advantag\$ | 2 |
| <input type="checkbox"/> | L13 | L12 near10 length | 65 |
| <input type="checkbox"/> | L12 | trace near5 pin | 1629 |
| <input type="checkbox"/> | L10 | spac\$ near5 clock pin | 1 |
| <input type="checkbox"/> | L9 | amd k7 same pentium | 1 |
| <input type="checkbox"/> | L8 | amd k7 near10 pentium | 1 |
| <input type="checkbox"/> | L7 | L5 near15 (switch\$ or multiplex\$ or mux) | 14 |
| <input type="checkbox"/> | L6 | L5.ti,ab. | 19 |
| <input type="checkbox"/> | L5 | (first type or second type) near3 (cpu or \$processor) | 369 |
| <input type="checkbox"/> | L4 | (various or different) adj type near3 (cpu or \$processor) | 612 |
| <input type="checkbox"/> | L3 | (various or different) adj type near3 (cpu or \$processo) | 126 |
| <input type="checkbox"/> | L1 | (first type or second type) near3 (cpu or \$processo) | 33 |

END OF SEARCH HISTORY

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